

<u>795</u>	... Viterbi decoding
<u>796</u>	... Branch metric calculation
<u>797</u>	.. Majority decision/voter circuit
<u>798</u>	. Error detection for synchronization control
<u>799</u>	. Error/fault detection technique
<u>800</u>	.. Parity bit
<u>801</u>	... Parity generator or checker circuit detail
<u>802</u>	... Even and odd parity
<u>803</u>	... Parity prediction
<u>804</u>	... Plural dimension parity check
<u>805</u>	... Storage accessing (e.g., address parity check)
<u>806</u>	.. Constant-ratio code (m/n)
<u>807</u>	.. Check character
<u>808</u>	... Modulo-n residue check character
<u>809</u>	.. Code constraint monitored
<u>810</u>	... Multilevel coding (n>2)
<u>811</u>	.. Forbidden combination or improper condition
<u>812</u>	... Specified digital signal or pulse count
<u>813</u>	... Two key-down detector
<u>814</u>	... Data timing/clocking
<u>815</u>	... Time delay/interval monitored
<u>816</u>	... Two-rail logic
<u>817</u>	... Noise level
<u>818</u>	... Missing-bit/drop-out detection
<u>819</u>	.. Comparison of data
<u>820</u>	... Plural parallel devices of channels
<u>821</u> Transmission facility
<u>822</u>	... Sequential repetition
<u>823</u> True and complement data
<u>824</u>	... Device output compared to input

FOREIGN ART COLLECTIONS

FOR000 **CLASS-RELATED FOREIGN DOCUMENTS**

Any foreign patents or non-patent literature from subclasses that have been reclassified have been transferred directly to FOR Collections listed below. These Collections contain ONLY foreign patents or non-patent literature. The parenthetical references in the Collection titles refer to the abolished subclasses from which these Collections were derived.

MEMORY TESTING (371/21.1) **DIGITAL LOGIC TESTING (371/22.1)** **DIGITAL DATA ERROR CORRECTION (371/30)**

FOR100 . Scan path testing (LSSD) (371/22.3)

FOR101 . Including test pattern generator (371/27)

FOR102 . Block code (371/37.1)

FOR103 .. Memory access (371/40.1)

FOR104 . Convolutional code (371/43)

FOR288 **ERROR/FAULT ANTICIPATION (371/4)**

. Replacement with spare device or system (371/8.1)

L Number	Hits	Search Text	DB	Time stamp
1	4	("6378125" "6230119" "5838975" "6378124") and switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 13:58
2	1	717/129.ccls. and context with switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 13:59
3	120	717/\$.ccls. and context with switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 13:59
4	108	717/\$.ccls. and context with switch and processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 13:59
5	13	717/\$.ccls. and context with switch and processor same breakpoint	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 13:59
-	284	717/124.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/14 17:19
-	5624	717/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/14 17:18
-	0	717/124.ccls. and (multi adj thread\$4 with processor) same breakpoint\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/14 17:20
-	0	717/124.ccls. and (multi adj thread\$4 with processor) same (break adj point\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/14 17:20
-	1	717/124.ccls. and (multi adj thread\$4 with processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/14 17:21
-	0	717/124.ccls. and (multi adj thread\$4 with processor) same debug	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/14 17:21
-	0	717/\$.ccls. and (multi adj thread\$4 with processor) same debug	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/14 17:22
-	17	717/\$.ccls. and (break adj point) same processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/14 17:24
-	2	717/\$.ccls. and (break adj point) same processor and (multi adj thread\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/14 17:37

-	0	717/\$.ccls. and (break adj point) same processor and (multi adj thread\$4) and (micro adj engin\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/14 17:38
-	0	(break adj point) same processor and (multi adj thread\$4) and (micro adj engin\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/14 17:38
-	354	(break adj point) same count\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 12:35
-	4	(break adj point) same count\$4 same alu	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 12:35
-	2	(break adj point) same count\$4 same alu and library	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 12:37
-	3	(break adj point) same count\$4 same alu and (library or record)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 13:06
-	0	(break adj point) same count\$4 same alu and (library or record) near (context with switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 12:51
-	0	(break adj point) near (context with switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 12:51
-	0	(break adj point) near (context with switch\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 12:52
-	7411	context with switch\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 12:52
-	0	(context with switch\$4) near (break adj point\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 12:52
-	1	(break adj point) same count\$4 same alu and (library or record) and processor same (multiple or many) with instruction\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 13:11
-	15	("6378125" "6230119" "5838975" "6378124") and processor same (multiple with instruction\$4 or program\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 14:52
-	6	("6378125" "6230119" "5838975" "6378124") and processor same (multiple with instruction\$4 or program\$4) and register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/20 13:56